



Application Serial No. 10/822,506 - Filed April 12, 2004

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REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2671

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/822,506

Filed: April 12, 2004

Inventor(s):

John V. Sell

Title: CPU AND GRAPHICS
UNIT WITH SHARED
CACHE

§ Examiner: Singh, Dalip K.
§ Group/Art Unit: 2671
§ Atty. Dkt. No: 5500-98000

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Rory D. Rankin

Printed Name

Signature

September 15, 2005

Date

*Okay to
enter
amendment.*
Dalip Singh
AV2671

RESPONSE TO OFFICE ACTION OF
JULY 15, 2005

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of July 15, 2005, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.